



Call for Papers

I-SPAN 2008

The 9th International Symposium on Parallel Architectures, Algorithms and Networks
May 7-9, 2008
Sydney, Australia

Organized by:
School of Information Technologies,
The University of Sydney
URL: <http://www.it.usyd.edu.au/~ispan08>

Scope of Symposium

The International Symposium on Parallel Architectures, Algorithms, and Networks (I-SPAN) is a forum for scientists, engineers, and practitioners throughout the world to exchange ideas and research results related to the design, use, analysis, and application of parallel architectures, algorithms, networks, and internet technology.

I-SPAN 2008 conference proceedings will be published by **IEEE Computer Society Press**.

The first eight symposia were held:

- I-SPAN '94: in Kanazawa, Japan, on December 14-16, 1994
- I-SPAN '96: in Beijing, China, on June 12-14, 1996
- I-SPAN '97: in Taipei, Taiwan, on December 18-20, 1997
- I-SPAN '99: in Perth/Fremantle, Australia, on June 23-25, 1999
- I-SPAN 2000: in Dallas/Richardson, Texas, USA, on December 7-9, 2000
- I-SPAN 2002: in Manila, the Philippines, on May 23-25, 2002
- I-SPAN 2004: in Hong Kong, China, on May 10-12, 2004
- I-SPAN 2005: in Las Vegas, USA, on December 7-9, 2005

The ninth I-SPAN symposium will be held in Sydney, Australia, on May 7-9. I-SPAN 2008 will include keynote addresses, invited/contributed papers, workshops, and special sessions on on-going projects on high-performance computing and/or communications. The symposium will have five tracks:

- Algorithms
- Applications
- Architecture
- Internet Technology and Informatics
- Networks and Communications

Relevant topics include (but are not limited to) the following:

- Applications of Parallel Processing (including web applications, peer to peer computing, grid computing and scientific applications)
- Bioinformatics and Computational Biology
- Compilers and Optimizations for Parallel Processing
- Database and Data Mining
- Fault Tolerance Issues and Systems
- Intelligent Networks
- Interconnection Networks
- Internet and WWW Measurement, Mapping and Modelling
- Internet Technology and Applications
- Multicasting and Broadcasting Issues
- Multimedia Networking
- Network Routing and Communication Algorithms (wired, wireless, ad-hoc, etc.)
- Network Security Applications
- Parallel and Network Computing and Algorithms
- Parallel Architecture and Processing Systems
- Scientific and High Performance Computing
- Telecommunication Network Design and Optimisation
- VLSI Network Design
- Wireless Networks and Mobile Computing

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Submission Guidelines

Please submit your complete paper(s) through the EDAS submission system at <http://edas.info>. The paper should not exceed 20 double-spaced pages of text using 12-point size type on 8.5 x 11 inch pages.

Workshop papers should be submitted to the individual workshop organisers. The workshops to be held will be separately advertised and also listed on the conference web site shortly after March 30.

Electronic submissions must be in the form of a readable PDF file containing the complete manuscript (including title, author's name, affiliation, and abstract). Authors should make sure that the PDF file will print on a PostScript printer that uses 8.5 x 11 inch letter size paper.

The results presented in the paper must be unpublished and not submitted for publication elsewhere, including the proceedings of other conferences or workshops. One author of each accepted paper will be expected to present the paper at the conference.

Proceedings will be published by the IEEE Computer Society Press.

Workshop proposals should be submitted to the workshop co-chairs.

Important Dates

Workshop proposal submissions	August 25, 2007
Panel proposal submissions	August 25, 2007
Deadline for Submission of Papers:	October 19, 2007 extended deadline
Submission of workshop papers	October 19, 2007 extended deadline
Notification of Acceptance:	December 1, 2007
Camera Ready Manuscript Due:	December 20, 2007
Advanced Registration for Authors:	December 20, 2007

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